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Embedded Systems provides readers with a developer's perspective to embedded systems concepts. It examines in detail each of the important theoretical and practical aspects that one must consider when designing today's applications. Readers will then be taken from concept to realization as they learn how to apply critical concepts. Throughout the pages, the Verilog language is used as a modeling and synthesis tool to express the hardware implementation, UML and structured design to model the software designs, and the C language to affect the software implementation.