

*Librería*  
***Bonilla y Asociados***  
*desde 1950*



**Título:** Advanced Digital Logic: State Machine Design Using Vhdl, Verilog, And Synthesis

**Autor:** Lee, Sunggu

**Precio:** \$474.00

**Editorial:**

**Año:** 2004

**Tema:**

**Edición:** 1<sup>a</sup>

**Sinopsis**

**ISBN:** 9780534466022

This textbook is intended to serve as a practical guide for the design of complex digital logic circuits such as digital control circuits, network interface circuits, pipelined arithmetic units, and RISC microprocessors. It is an advanced digital logic design textbook that emphasizes the use of synthesizable VHDL code and provides numerous fully worked-out practical design examples including a Universal Serial Bus interface, a pipelined multiply-accumulate unit, and a pipelined microprocessor for the ARM THUMB architecture.